#### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Attorney Docket No.: TRAN-P191

Inventor(s):

Mike Pelham and James B. Burr

Serial No.:

Group Art Unit:

Filed:

10/10/03

Examiner:

Title:

LAYOUT PATTERNS FOR DEEP WELL REGION TO FACILITATE ROUTING BODY-

BIAS VOLTAGE

Commissioner of Patents P. O. Box 1450 Alexandria, VA 22313-1450

Sir:

Information Disclosure Statement Submitted Pursuant to 37 C.F.R. 1.97(b)

The citations referenced herein, copies attached, may be material to the examination of the above-identified application and are, therefore, submitted in compliance with the duty of disclosure as defined in 37 C.F.R. 1.56. The Examiner is requested to make these citations of official record in the application.

This Information Disclosure Statement submitted in accordance with 37 C.F.R. 1.97(b) is not to be construed as a representation that a search has been made, that additional items material to the examination of this application do not exist, or that any one or more of these citations constitute prior art under 35 U.S.C. 102.

The Examiner's attention is respectfully directed to the following U.S. Patents:

<u>Pat. No.</u>	Pat. Title	<b>Grant Date</b>
6,489,224	METHOD FOR ENGINEERING THE THRESHOLD VOLTAGE OF A DEVICE USING BURIED WELLS	12/03/02
6,091,283	SUB-THRESHOLD LEAKAGE TUNING CIRCUIT	07/18/00
6,087,892	TARGET ION/IOFF THRESHOLD TUNING CIRCUIT AND METHOD	07/11/00
6,048,746	METHOD FOR MAKING DIE-COMPENSATED THRESHOLD TUNING CIRCUIT	04/11/00
6,303,444	METHOD FOR INTRODUCING AN EQUIVALENT RC CIRCUIT IN A MOS DEVICE USING RESISTIVE WELLS	10/16/01
6,218,708	BACK-BIASED MOS DEVICE AND METHOD	04/17/01

Please direct all correspondence concerning the above-identified application to the following address:

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Respectfully submitted,

Date: (0/10/2003

Anthony C. Murabito Reg. No. 35,295

Attorney Docket No.: TRAN-P191

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#### Patent Application

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### Form 1449

#### **U.S. Patent Documents**

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub- class	Filing Date
	Α	6,489,224	12/03/02	Burr	438	526	05/31/01
	В	6,091,283	07/18/00	Murgula, et al.	327	537	02/24/98
	С	6,087,892	07/11/00	Burr	327	534	06/08/98
	D	6,048,746	04/11/00	Burr	438	17	06/08/98
	E	6,303,444	10/16/01	Burr	438	289	10/19/00
	F	6,218,708	04/17/01	Burr	257	372	02/25/98
	G						
	Н						
	J						
	K					T	

Foreign Patent or Published Foreign Patent Application

Examiner		Document	Publication	Country or		Sub-	Trans	lation
Initial	No.	No.	Date	Patent Office	Class	class	Yes	No
	L					1		
	М							
	N							
	0							
	Р						1	
	Q							

#### **Other Documents**

Other Documents					
Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication			
	R				
	S				
	Т	·			
Examiner	1	Date Considered			

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.